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Sheet 1 of 1

Form PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION	Docket Number	Application Number
	751	08/464,034
	Applicant	
	Gilbert Hyatt	
	Filing Date	Group Art Unit
	June 5, 1995	

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS
	3,579,201	05/71	Langley	712	245
	3,657,706	04/72	Horgan et al	345	2.1
	3,675,219	07/72	Kawasaki et al	365	73
	4,559,555	12/85	Schoolman		
	4,669,812	06/87	Hoebing	359	9
	4,674,046	06/87	Ozeki et al	382	131
	4,737,921	04/88	Goldwasser et al	345	421
	5,093,567	03/92	Staveley	250	221
	5,130,794	07/92	Ritchey	348	39
	5,135,397	08/92	Yen	434	2
	5,255,211	10/93	Redmond	703	6

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

Ferrite Memory systems by Russel, Loulis A. et al., dated IEEE Transactions on Magnetics vol. mag-4, no. 2 June 1968, pp. 134-145.

Current Status of Large Scale Integration Technology by Petritz, Richard L., IEEE Journal of Solid State Circuits vol. sc-2, no. 4 December 1967.

Designing a System on a Chip by Spitalny, Arnold, annual ACM IEEE Design Automation Conference Proceeding of the June 1970 design automation workshop of Design automation, pp. 336-345

EXAMINER	DATE CONSIDERED 4/28/04
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	<p>"4004 4-bit Central Processor Unit", Intel Chip Description (October 1, 1970) pp. 1 and 2.</p> <p>"A High-Speed Algorithms for the Computer Generation of Fouiour Transforms," H. Andrews, IEEE transactions on Computers, vol. 17, no. 4, 1968, pages 146-148.</p> <p>"A Multiresolution Spline With Application to Image Mosaics," Peter, J. Burt and Edward H. Adelson, ACM Transactions on Graphics, vol. 2, no. 4, October 1983, pages 217-236.</p> <p>"Parallel Processing Image Synthesis and Anti-Aliasing" by Richard Weinberg, Computer Graphics, Vol. 15, No. 3, pp. 55-62, August, 1981.</p> <p>"Pixel-Planes: A VLSI-Oriented Design for a Raster Graphics Engine" by Henry Fuchs and John Poulton, VLSI Design, Third Quarter 1981, pp. 20-28.</p> <p>"The Laplacian Pyramid as a Compact Image Code," Peter, J. Burt and Edward H. Adelson, IEEE Transactions on Communications, vol. Com-31, No. 4, April, 1983, pages 532-540.</p> <p>"The Motivation for Interactive Computer Graphics", Ivan Sutherland 6/1970, pp. 7-8.</p> <p>#1970s Motorola History Highlights (2 pages)</p> <p>#1980s Motorola History Highlights (2 pages)</p> <p>#CHM: Timeline of Computer History (11 pages)</p> <p>#Intel Microprocessor Hall of Fame (3 pages)</p> <p>Add. "Central processor made on single MOS chip" (Electronic Design 22), 10/25/70, p.30.</p> <p>Add. "'CPU on a chip' goes public" (EDN), 5/71, p. 17.</p> <p>Add. "MOS: the thrust is economy, complexity and volume" (Electronic Design), 6/10/71, p. 34.</p> <p>Anuta, Digital Registration of Multispectral Video Imagery, SPIE Journal, Vol. 7, No. 6, Sept. 1969, pp. 168-175.</p> <p>Anuta, Spatial Registration of Multispectral and Multitemporal Digital Imagery Using Fast Fourier Transform Techniques, IEEE Trans.Geo.Elec., Vol. GE-8, No.4, Oct. 1970, pp.353-368.</p> <p>Atley, "LSI poses dilemma", 1970, pages 44-52, Electronic Design 3.</p> <p>Beelitz, H.R. et al., System architecture for large scale integration, RCA Laboratories, Princeton NJ, Fall Joint Computer Conference, 1967 pp. 185-200.</p> <p>Berkley et al., "Computers Their Operation and Applications", Reinhold Publishing Corp., pages 30, 33, 355, 1956.</p>			

EXAMINER	DATE CONSIDERED 4/26/84
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

Form PTO—1449	Docket Number 751	Application Number 08/464,034
INFORMATION DISCLOSURE CITATION IN AN APPLICATION	Applicant Gilbert Hyatt	
	Filing Date June 5, 1995	Group Art Unit

Brickman, N.F., LARGE SCALE INTEGRATION, IBM Tech. Discl. Bul. pp. 1927-1928, Vol. 13 No. 7, Dec. 1970.

Brickman, N.F., REDUNDANT MEMORY CELL AND DECODER, IBM Tech. Discl. Bul., Vol. 13 No. 7, Dec. 1970.

C.J. Hughes et al, "LSI in telephone exchange peripheral units," 6/3/69, pp. 33-34.

Collingwood preface, Collins et al., editors, "Machine Intelligence 1", American Elsevier Publishing Co. Inc., 1967.

Current Status of Large Scale Integration Technology by Petritz, Richard L., IEEE Journal of Solid State Circuits vol.sc-2, no. 4 December 1967.

David L. Hudson, "A practical solution using a new approach to robot vision," SPIE Architecture and Algorithms for Digital Image Processing, Volume 435, August 1983, pp. 221-228.

Designing a System on a Chip by Spitalny, Arnold, annual ACM IEEE Design Automation Conference Proceeding of the June 1970 design automation workshop of Design automation, pp. 336-345.

Dichter et al., "A New Approach to CGI Systems", 1980.

Disclosure document 131747, "TECHNOLOGICAL INNOVATIONS", Gilbert P. Hyatt, inventor; dated October 17, 1984.

Disclosure Document 131747 Retention Label, "TECHNOLOGICAL INNOVATIONS", Gilbert P. Hyatt, inventor; dated October 17, 1984.

Doctor et al, "Display Techniques for Octree-Encoded Object" (July 1981), pp. 29-36.

Eliane Egeli et al., "A hardware and software optimized program system for interactive image processing," SPIE Architecture and Algorithms for Digital Image Processing, Volume 435, August 1983, pp. 134-138.

Encyclopedia.com definition of "artificial intelligence", May 3, 2001, 2 pages.

Fairchild Semiconductor linear integrated circuit data catalog describing Fairchild 710 comparitor, pp. 32-36.

Fairchild, "9401 CRC GENERATOR/CHECKER", Macrologic Bipolar Microprocessor Databook, pp-3/11-3/14, 1976.

Ferrite Memory systems by Russel, Loulis A. et al., dated IEEE Transactions on Magnetics vol. mag-4, no. 2 June 1968, pp. 134-145.

Flynn, "CRT terminals talking to computers" (Electronic Products Magazine), 6/70, pp. 22-29.

Foley et al, Computer Graphics: Principles & Practice, p. 4-7, 201-203, 230-231, 350-351, 364-371, 864-871 (1992).

Fu et al., Information Processing of Remotely Sensed Agricultural Data, Proc. IEEE, Vol. 57, No. 4, April 1969, pp. 639-653.

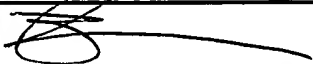
IEEE Transactions on Automatic Control, Vol. AC-28, No. 3, "Multiconfiguration Kalman Filter Design for High-Performance GPS Navigation" by Kao et al. (March 1983).

Intel, "4004 SINGLE CHIP 4-BIT P-CHANNEL MICROPROCESSOR", undated. Pages 8-15 to 8-23, Intel.

Ivan E. Sutherland, "Computer Displays", Scientific American Vol. 222, No. 6, June 1970, pp. 57-81.

Jain et al., "Displacement Measurement and its Application in Interframe Image Coding", IEEE Transaction on Communications, vol. Com-29, no. 12, December 1981, pp. 1799-1808.

Kashiwazaki, "Invoice Letter of Sale", June 20, 1970. pages 1-3.

EXAMINER 	DATE CONSIDERED 4/26/04
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Kenneth McConnell, Fax: Facsimile Technology and Systems, Artech House Publishing, 1999, ISBN 0-89006-944-1, pp. 12-20.

Kiver editor, "Television Simplified", seventh edition, Van Nostrand Reinhold Co., pages 39-41, 1973.

Kiver et al., Television Electronics: Theory and Servicing, eighth edition, Van Nostrand Reinhold Company, pp. 98-103 (1983).

Kornstein, H. Dynamic RAM Refresh in 8080 systems, New Electronics, July 1976, vol. 9, no. 15, pg 15.

LisaGraph brochure, the Apple Computer Inc., published in 1983, 4 pages.

Mary Whitton, "Memory Design for Raster Graphics Displays", IEEE CG & A, pages 48-65 (3/84).

Meagher, Donald: "Efficient Synthetic Image Generation of Arbitrary 3-D Objects", 1982 IEEE, page 473-478.

Microsoft Computer Dictionary, Fifth Edition, Microsoft Corporation, pp. 267, 372 (2002).

Miller, "Machine Vision for Robotics and Automated Inspection", Vol. 1, Technical Insights, Inc., page 9, 1983.

Mokhoff, "The Continuing Quest for 3-D Television", 1981.

N. Ahmed, T. Natarajan, and K. R. Rao, Discrete Cosine Transform, IEEE Transactions on Computers, Jan. 1974, pp. 90-93.

Nagy, Digital Image-Processing Activities in Remote Sensing for Earth Resources, Proc. IEEE, Vol. 60, Oct. 1972, pp. 1177-1200, reprinted in Machine Recognition of Patterns (Ashok K. Agrawala ed., IEEE Press 1976), pp. 381-404.

Neil H. Endsley et al., "A practical architecture for a real-time image resampling processor," SPIE Architecture and Algorithms for Digital Image Processing, Volume 435, August 1983, pp. 181-186.

Newman et al, "Principles of Interactive Computer Graphics" second edition (1979), pp. 3-15, 25-27, 103-106, 147-158, 170-171, 183-210, 229-259, 277-289, 293-307, 333-366, 386-387, 398-402, 413-441, 510-512.

Newman et al, "Principles of Interactive Computer Graphics" second edition (1979), pp. 53-61.

Newman et al, "Principles of Interactive Computer Graphics" second edition (1979) pp. 229-245, 411-478.

Newman et al, "Principles of Interactive Computer Graphics," second edition, pp. 154-156, 249-259, 294-297, 413-424 (1979).

Noyce et al. "Provisional Agreement", 04/28/1969, pages 1-3, Intel.

Pavlidis, "Structural Pattern Recognition", Springer-Verlag, pages 1-2, 1977.

Pearson, editor, Proceedings of SPIE - The International Society for Optical Engineering, "Processing and Display of Three-Dimensional Data", vol. 367, pp. 160-164.

Pertinent Concepts in Computer Graphics edited by M. Faiman and J. Nievergelt (03/1969).

Phong, "Illumination for Computer Generated Pictures" (June 1975), pp. 311-317.

Rotherberg, Van Nostrand, Cassand, et al., "Seismic Filtering", Society of Exploration Geophysicists (1971) (1966).

Say et al. editors, "Analog and Digital Computers", Philosophical Library Inc., pages 236-237, 1960.

EXAMINER	DATE CONSIDERED 4/26/84
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Setty, editor, Proceedings of SPIE - The International Society for Optical Engineering, "Visual Simulation and Image Realism II", vol. 303, 1981, pp. 54-66.
 Shantz, "Two-pass warp algorithm for hardware implementation", SPIE Vol. 367, Processing and Display of Three-Dimensional Data, pages 160-164, 1982.
 Sheriff, "Encyclopedic Dictionary of Exploration Geophysics", Society of Exploration Geophysicists (1973).
 Sherr, "Fundamentals of Display System Design," pp. 162-164, 180-184, 260-268 (1970).
 Stevens, Computer Graphics Dictionary, Charles River Media, Inc., pp. 217, 239, 314 (2002).
 Sutherland, "A Head-Mounted Three Dimensional Display," pp. 757-764 (1968).
 The American Heritage Dictionary, Second College Edition, page 300, 1982.
 THE MOTIVATION FOR INTERACTIVE COMPUTER GRAPHICS; Scientific American 6/1970, pp. 7-8.
 The New IEEE Standard Directory of Electronic Terms 617 (5th ed. 1993).
 Three page document allegedly from Nippon Calculating Machine Sales Corporation to Intel Corp.
 Traub, "Stereoscopic Display Using Rapid Varifocal Mirror Oscillations", 1967.
 Vadasz, "Desk Calculator Chips for ETI", 04/22/1970, pages 1-9, Intel.
 Van Nostrand's Scientific Encyclopedia, Fourth edition, Van Nostrand Co. Inc., pages 125 and 1753, 1968.
 Webster's Third New International Dictionary, G&C Merriam Company, pages 1912, 2577, 1967.
 Wolfgang K. Giloi, Interactive Computer Graphics Data Structures, Algorithms, Languages. Prentice Hall, Inc., Englewood Cliffs, New Jersey, 1978, pp. 7 and 8.

EXAMINER

BRIAN WERNER
PRIMARY EXAMINER

DATE CONSIDERED

4/26/09

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